

**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Ichio YUDASAKA et al.

Attn: PCT Branch

Application No.: 09/936,041

Docket No.: 110553

Filed: September 7, 2001

For: METHOD OF MANUFACTURING A THIN-FILM TRANSISTOR

**RESPONSE TO NOTIFICATION OF MISSING REQUIREMENTS  
UNDER 35 U.S.C 371 IN THE UNITED STATES DESIGNATED/  
ELECTED OFFICE (DO/EO/US) WITH DECLARATION**

Director of the U.S. Patent and Trademark Office  
Washington, D.C. 20231

Sir:

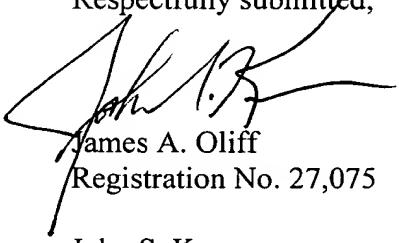
In response to the Notification of Missing Requirements Under 35 U.S.C 371 in the United States Designated/Elected Office (DO/EO/US) (copy attached) mailed on October 19, 2001, submitted herewith is the executed Declaration of the inventors. Any specification attached to and referenced in the Declaration is a copy of the specification and any amendments thereto which were filed in the Office in order to obtain a filing date for the application.

Attached is our Check No. 126095 for  \$130.00  \$65.00 (entitlement to small entity status is asserted) for the fee under 37 C.F.R. §1.492(e).

Entry of these documents should complete all of the filing formalities and fully satisfy all requirements of the Notification of Missing Requirements. Accordingly, prompt issuance of a Notification of Acceptance and Official Filing Receipt, and prompt examination and allowance of this application are respectfully solicited.

The Director is hereby authorized to charge any additional fee (or credit any overpayment) associated with this communication to Deposit Account No. 15-0461. Two duplicate copies of this paper are attached.

Respectfully submitted,



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JAO:JSK/kap

Date: December 18, 2001

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